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|  | Intel 8080 | MIPS | ARM | Nvidia Fermi |
| RISC vs CISC | CISC | RISC | RISC | Looks RISCy and feels CISC |
| Instruction Size | 8-bit | 32-bit | 32-bit+ | 64 |
| General Purpose Registers | 7 8-bit registers | 32 | 16 32-bit registers | 63 |
| Endian | Little | Big | Big | Little |
| Instruction Encoding | Variable | Fixed | Fixed | Fixed |
| Max Operands | 2 | 3 | 3 | 2 |
| Instruction Examples: |  |  |  |  |
| Add | LDA a (load a to memory)  ADD S (add register to A) | Add $d, $s, $t  ($d = $s + $t) | ADD R0, R1, R2  (R0 = R1 + R2) | 1100 000000 1110 000000 000000 0000000000000000000000 0000000000 010010         mod        reg0   reg1                  immea  IADD R1, R2 |
| Move | MOV D, S (move S into D) | Move $t2, $t3  ($t2 = $t3) | Mov R4, R5  (R5 is copied into R4) | 0010 011110 1110 000000 000000 0000000000000000000000 0000000000 010100         mod        reg0   reg1                  immea  Mov, R1, R2 |